

Spiking Neural P System with Shunting Inhibition

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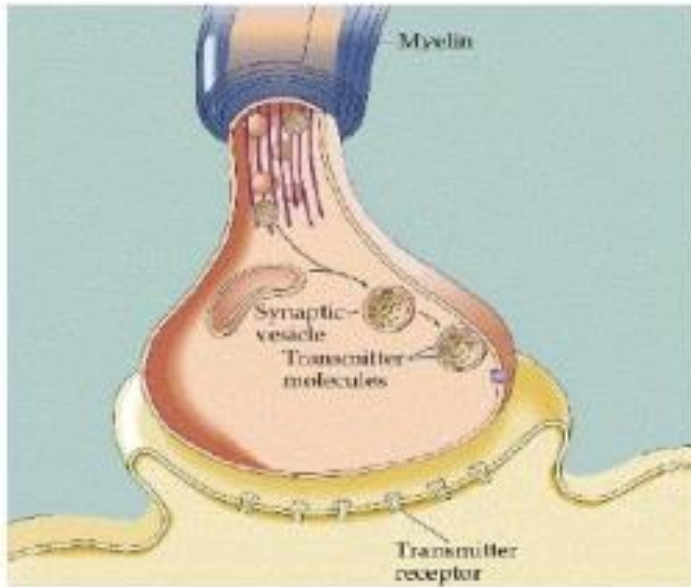
- Implementation of Arithmetic Operation^[1]
- Division: Loop^[2]
- Shunting Inhibition^[3]

[1] M.A. Gutierrez-Naranjo, et al. IJCCC, 2009, 4(3): 244-252.

[2] X. Zeng, et al. IEEE Transactions on Nanobioscience, 2012, 11(4): 366-374.

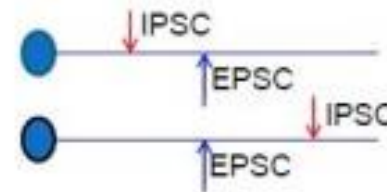
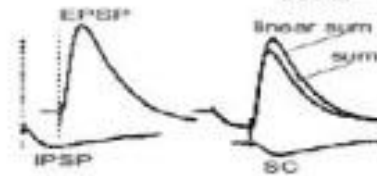
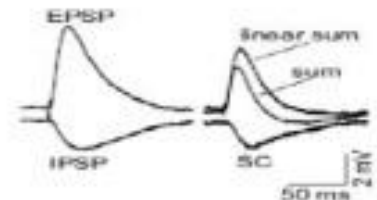
[3] S. Wu, CCF ADL81, Beijing, 2017 7.28-30.

Computation @ Synapse



Synapse Efficacy=Connection Weight

Shunting inhibition=
Multiplication/Division



IPSC: inhibitory postsynaptic current
EPSC: excitatory postsynaptic current

- Model: delay or synapse?
- Computation?

Thanks for your attention!